Homework 6 Build Systems

Assigned: Friday, February 12, 11:00AM

Due: Friday, February 19, 11:00AM (Hard Deadline)

Submission Instructions

Submit this assignment on Gradescope. You must submit every page of this PDF. We recommend using the free online tool PDFescape to edit and fill out this PDF. You may also print, handwrite, and scan this assignment.

Even if there are not questions to answer on the first page, please still submit every page of this PDF.

There may multiple answers for each question. If you are unsure, state your assumptions and your reasoning for why you think your answer makes sense.

For this assignment, we will experiment in the EECS 280 W15 repository you created for Homework 4.

1 Expressing Dependencies

- 1. Run make to build everything.
- 2. Run make again (nothing happens).
- 3. Edit p2.cpp and make a change (add a comment or something).
- 4. Run make again.
- 5. Edit p2.h and make a change (add a comment or something).
- 6. Run make again.

Why did make rebuild things after step 4 but not after step 6? Why is this a problem?

Rewrite the rule for simple_test so that make rebuilds correctly for any changes you make:1

Think this is a pain? Check out the advanced homework for a $BetterWay^{TM}$.

2 From Build Engine to Rules Engine

Makefiles are often asked to do more than simply build your software. A common example is a rule named clean that deletes everything built by the Makefile.

- 1. Run make to build everything.
- 2. Run make clean to delete everything that was built.
- 3. Run make to build everything.
- 4. Run touch clean
- 5. Run 1s (do you understand what touch does?)
- 6. Run make clean

Why did make run the clean rule after step 2 but not after step 5?

What flag can you pass to make so that it unconditionally "builds" the clean target?

make -__ clean

Describe how to fix the Makefile so that fake targets like clean work correctly.

¹You do not need to worry about system header files

3 Removing Duplicated Effort

Notice that currently the all target and the test target have the same list of dependencies.

List <u>all</u> the changes you have to make to the Makefile so that the **test** target correctly depends on the <u>all</u> target in all cases.

4 Anything Special about All?

Currently, if you just type make, make will run make all. One might wonder why make chooses the all goal by default. While you could look this up, we are computer *scientists*. Make changes to the Makefile until you are confident that you understand how make chooses the default goal.

Describe the experiments you ran in order to determine what target make builds by default.

5 Pulling Some Pieces Together

Option 3 from Question 3 on Homework 3 (during week 3!) noted:

First cool thing that happened: Yes, you can run make without any Makefile anywhere. We'll cover how that happened during build-system week.

First let's get a simple environment set up and try some things out:

```
> mkdir /tmp/wk6 && cd $_
> echo -e '#include <stdio.h>\n\nint main() {\n\tprintf("Howdy\\n");\n\treturn 0;\n}\n' > hello.c
> make
> make hello
> ./hello
Does make clean work? Why not?

Now try
> rm hello
> make -r hello
What does the -r flag do?

Next try
> make CFLAGS=-03 hello
What changed when hello was built this time?

Finally run
> make hello -p | less
```

Find the built-in rule that runs to create "hello" from "hello.c" and copy it here:

```
Now let's add an additional file to the mix, only a C++ file this time:

(This example uses a special shell syntax for easily writing multiple lines to a shell command)

> cat << MARKER > wazzup.cpp

#include <iostream>

int main() {
    std::cout << "Wazzup?" << std::endl;
    return 0;
}

MARKER

> cat wazzup.cpp
```

Using what you have learned, write a single make command (i.e. only call make once) that, without a Makefile, will build both "hello" and "wazzup", but builds hello optimized for speed (-O3) and wazzup optimized for size (-Os).